ALEXANDER SCOTT

2300 Maple Ave, Apt 177, Torrance, CA 90503 | 860-670-6187 | alexscott233@gmail.com

EDUCATION

The George Washington University, Washington, D.C. Class of 2015
Bachelor of Science in Electrical Engineering

Benjamin C. Cruickshanks Award	Highest Academic Standing Department Wide
Philip B Kaplan Award	Highest GPA in Electrical Engineering
Abdelfattah Abdalla Prize	Outstanding Scholarship and Service

PROFESSIONAL EXPERIENCE

Raytheon Space and Airborne Systems, El Segundo, CA
Electrical Engineer June 2015 - Present

- Core Electronics Product Line FPGA Team Member
  - Supported FPGA test and integration at module and unit level for various Core Electronics Products
  - Digital Worst Case Circuit Analysis team lead
  - HDL Partition redesign for reuse across various Core Electronics Products

Smart Systems Lab, The George Washington University, Washington, D.C.
Undergraduate Research Assistant May 2014 – May 2015
Energy Scavenging for Mobile Robots Project

- Designed and assembled hardware on the robot
- Simulated robot navigation behavior in MATLAB
- Presented research findings

The George Washington University, Washington, D.C.
School of Engineering and Applied Science Tutor September 2013 – May 2015

- Prepare and delivere review lecture material for weekly review sessions
- Explain and reiterate complex topics to student peers

TECHNICAL EXPERIENCE

Autonomously Navigating Robot with Terrain 2016
Mapping and Visual Target Recognition

- Responsible for running worst case circuit analysis on FPGA designs
  - Analysis based on Clock Domain Crossings, Functional Simulation, Simulation Code Coverage, IO Termination Checking, SSO Analysis, and Code Linting
- Developing scripting for tool usage for automatic execution and processing of data for easy analysis

FPGA Test Integration Engineer 2015

- Developed test plan for FPGA to verify requirements and functional operation at module circuit level
- Created stimulus for lab testing as well as computer simulation
- Digital circuit debug and design modification

Autonomously Navigating Robot with Terrain 2015
Mapping and Visual Target Recognition

- Envisioned project idea and set team goals
- Lead team members in developing individual sub systems
- Designed robot platform and developed localization and navigation systems to fulfill project requirements

CERTIFICATIONS

Lean Six Sigma Certification 2016

TECHNICAL SKILLS

Software and Programming Languages: Windows, Linux, Microsoft Office, Matlab, Matlab HDL Coder, Xilinx ISE, Xilinx Vivado, Microsemi Libero, Aldec Alint, Mentor Graphics Questasim, Mentor Graphics Questa CDC, VHDL, Verilog, PSpice, Multisim, AVR C, EAGLE Cad, VLSI, Autocad 2015, C, C++, Python, VBA, TCL